

APPLICATION DATA SHEET

Applicant: 1st Inventor: TROTTER, Brian David
Residence: 5408 Apache Creek Cove
Austin, Texas 78735
Mailing: Same as above
Citizenship: United States of America

2nd Inventor: DUEWER, Bruce
Residence: 2511 Broken Oak Drive
Austin, Texas 78745
Mailing: Same as above
Citizenship: United States of America

3rd Inventor: MELANSON, John Laurence
Residence: 901 West 9th Street, Apt. 201
Austin, Texas 78703
Mailing: Same as above
Citizenship: United States of America

Correspondence: James J. Murphy, Esq.
Winstead Sechrest & Minick P.C.
P.O. Box 50784
Dallas Texas 75201

Application Information: Title: METHODS FOR OUTPUT EDGE-BALANCING IN
PULSE WIDTH MODULATION SYSTEMS AND
DATA CONVERTERS USING THE SAME
Classification: Class: Subclass:
Tech Center: 2819
Drawing Sheets: 20 Drawing to be Published: N/A
Docket No: 1454-C1 (P279P1)
Type: UTILITY
Secrecy: None

Representative Information: James J. Murphy, Reg. No.34,503
Steven Lin, Reg. No. 35,250
Scott Thomas, Reg. No. 39,855
Barry S. Newberger, Reg. No. 41,527

Domestic Priority: This is a Continuation-In-Part of
U.S. Serial No. 10/662,788 filed on September 15, 2003

CFR §119 Foreign Priority: None

Attorney Docket No.
1454-C1 (P279P1)

Patent

Assignee:

Cirrus Logic, Inc.
2901 Via Fortuna
Austin, Texas 78746

DALLAS_1\3912627\2
2836-P279P1 01/22/2004